MICROTEST

Automatic Testing Equipment Power Switch / LED Power Testing System PT-9200/PT-9300



Suitable functional testing for LED power supply and Switching Power products Easy to edit environment, easy to learn Suitable for continuous or single project test Suitable for AC-DC/DC-DC related power products Suitable for multiple sets testingThe fixtures can be used for double-cut to promote the efficiency Shop-Floor function and data integration Debug mode to get the testing certification easily Modular design, easy to maintain It supports Barcode Reader Statistical reporting capabilities

Standard

SCSI cable 8255 cable Fixture flat cable Power Cord User manual CD PC Link Software

Optional

AC to DC charger test fixture DC to DC charger test fixture GPIB cable

	Fixture	AC to DC / DC
	Number of output array	Up to 8 sets D
	Number of input array	1 set
	DC Power Supply	Purchase by s
	AC Power Supply	Purchase by s
	PWM dimming-1	60Hz - 1kHz /
	PWM dimming-2	60Hz - 100kHz
	SMBus Dimming	Control DS180
	Over Voltage Protectoion	Action confirm
	Short Protectoion	Action confirm
	Adapter Efficiency	0 - 100%
	P.F.	0 - 1
	Inrush Current	50A Max.
	Timing measurement	0 - 2 Sec reso
	Dynamic Load	THIGH & TI O
	5	
	Load Regulation	
	Load Regulation Line Regulation	DC Load Mod
	Load Regulation Line Regulation Combine Test	DC Load Mod
	Load Regulation Line Regulation Combine Test Bar Code	DC Load Mode

	Measuring	Item
Inrush Current		
Turn On Delay Time		
Rise Time (mS)		
Over Shoot (V) or (%)		
Hold up Time (mS)		
Fall Time		
Dynamic Load (V)		
Short Protection		
Over Current Protection		
Recovering (V)		
Over Voltage Protection (V)		

Functior

CERS232 Remote

C to DC adapter / charger fixture / LED Power fixtures

OC output

specification

specification

Step 1Hz , Duty 1 - 99% / Step 1%

Iz / Step 1Hz , Duty 1 - 99% / Step 1%

03 of DALLAS , Data (Series) , Clock (TTL)

olution 0.01mS

DW 50µ - 9.999 Sec (DC LOAD)

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pensation

PT-9200 ∕ PT-9300)
Regulation (V)
Line Regulation
Ripple & Noise(mVp-p)
Efficiency
Combine Test (V)
Pin (W)
PF
Vin (V)
lin(A)
linpk+(A)
linpk-(A)